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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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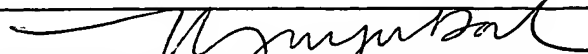
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Application Number	10/810,963
Filing Date	March 26, 2004
First Named Inventor	Rueckes, Thomas et al.
Art Unit	2818
Examiner Name	TBA
Attorney Docket Number	112020.146US2 NAN-22

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ^{2/(if known)}			
DN		US-2004/0085805A1	05-06-2004	SEGAL et al.	
DN		US-2004/0159833A1	08-19-2003	RUECKES et al.	
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Examiner Signature		Date Considered	11/02/05
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			Filing Date	March 26, 2004	
			First Named Inventor	Rueckes, Thomas et al.	
			Art Unit	2818	
			Examiner Name	TBA	
Sheet	2	of	4	Attorney Docket Number	112020.146US2 NAN-22

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Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ^{2(If known)}			
DN		WO 01/44796 A1	06-21-2001	Board of Trustees of the Leland Stanford Junior. University.	
DN		WO 01/03208 A1	01-11-2001	President and Fellows of Harvard College	

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
DN	A1	CHOI, W. B. et al., "Carbon-nanotube-based nonvolatile memory with oxide-nitride-film and nanoscale channel." <i>Appl. Phys. Lett.</i> , 2003, Vol. 82(2) 275-277.		
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DN	A4	WOLF, S., Silicon Processing for the VLSI Era; Volume 2 – Process Integration, Multi-Level-Interconnect Technology for VLSI and ULSI, 1990, Section 4.3 Materials for Multilevel Interconnect Technologies, pp. 189-191, Lattice Press, Sunset Beach	
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DN	A6	ROBINSON, L.A.W., "Self-Aligned Electrodes for Suspended Carbon Nanotube Structures." <i>Microelectronic Engineering</i> , 2003, Vols. 67-68, 615-622.	
DN	A7	TOUR, J. M. et al., "NanoCell Electronic Memories." <i>J. Am. Chem Soc.</i> , 2003, Vol. 125, 13279-13283.	
DN	A8	RUECKES, T., et al., "Carbon Nanotube-Based Nonvolatile Random Access Memory for Molecular Computing" <i>Science</i> , 2000, Vol. 289, 94-97.	
DN	A9	FAN, S. et al., "Carbon nanotube arrays on silicon substrates and their possible application." <i>Physica E</i> , 2000, Vol. 8, 179-183.	
DN	A10	ZHAN, W. et al., "Microelectrochemical Logic Circuits." <i>J. Am. Chem. Soc.</i> , 2003, Vol. 125, 9934-9935.	
DN	A11	SOH, H. T. et al., "Integrated nanotube circuits: Controlled growth and ohmic contacting of single-walled carbon nanotubes." <i>Appl. Phys. Lett.</i> , 1999, Vol. 75(5) 627-629.	
DN	A12	KINARET, J.M. et al., "A carbon-nanotube-based nanorelay", <i>Appl. Phys. Lett.</i> , 2003, Vol. 82(8) 1287-1289.	
DN	A13	FRANKLIN, N. R. et al., "Integration of suspended carbon nanotube arrays into electronic devices and electromechanical systems." <i>Appl. Phys. Lett.</i> , 2002, Vol. 81(5) 913-915.	
DN	A14	AVOURIS, P., "Carbon nanotube electronics," <i>Chem. Physics</i> , 2002, Vol. 281, pp. 429-445.	
DN	A15	DAI, H. et al., "Controlled Chemical Routes to Nanotube Architectures, Physics, and Devices." <i>J. Phys. Chem. B</i> , 1999, Vol. 103, 111246-11255.	
DN	A16	HOMMA, Y. et al., "Growth of Suspended Carbon Nanotubes Networks on 100-nm-scale Silicon Pillars." <i>Appl. Phys. Lett.</i> , 2002, Vol. 81(12) 2261-2263.	
DN	A17	AJAYAN, P.M., et al., "Nanometre-size tubes of carbon." <i>Rep. Prog. Phys.</i> , 1997, Vol. 60, 1025-1062.	
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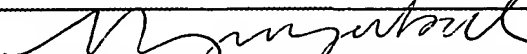
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Sheet	4	of	4	Attorney Docket Number	112020.146US2 NAN-22

DN	A19	VERISSIMO-ALVES, M. et al., "Electromechanical effects in carbon nanotubes: <i>Ab initio</i> and analytical tight-binding calculations." <i>Phys. Rev. B</i> , 2003, Vol. 67, 161401-1 - 161401-4.	
DN	A20	FUHRER, M.S. et al., "High-Mobility Nanotube Transistor Memory." <i>Nano Letters</i> , 2002, Vol. 2(7) 755-759.	
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DN	A22	FARAJIAN, A. A. et al., "Electronic transport through bent carbon nanotubes: Nanoelectromechanical sensors and switches." <i>Phys. Rev. B</i> , 2003, Vol. 67, 205423-1 - 205423-6.	
DN	A23	FISCHER, J.E. et al., "Magnetically aligned single wall carbon nanotube films: Preferred orientation and anisotropic transport properties." <i>Journal of Appl. Phys.</i> , 2003, Vol. 93(4) 2157-2163.	
DN	A24	LEE, K.H. et al., "Control of growth orientation for carbon nanotubes." <i>Appl. Phys. Lett.</i> , 2003, 82 (3) 448-450.	
DN	A25	CASAVANT, M.J. et al., "Neat macroscopic membranes of aligned carbon nanotubes." <i>Journal of Appl. Phys.</i> , 2003, Vol. 93(4) 2153-2156.	
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DN	A29	CUI, J.B. et al., "Carbon Nanotube Memory Devices of High Charge Storage Stability." <i>Appl. Phys. Lett.</i> , 2002, Vol. 81(17) 3260-3262.	

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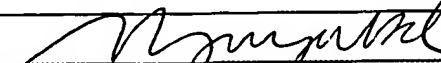
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DN		US-6,803,840	10-12-2004	HUNT et al.	
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DN		WO 04/065657 A1	08-05-2004	Nantero, Inc.	

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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²

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